

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5128	UART @ad<"20020102"	US-PGPU B; USPAT; USOCR; EPO; IBM_TDB	AND	ON	2005/03/20 16:42
L2	4993	L1 not "International Business Machines".as.	US-PGPU B; USPAT; USOCR; EPO; IBM_TDB	AND	ON	2005/03/20 18:15
L3	407	L2 and (UART same multiplex\$3)	US-PGPU B; USPAT; USOCR; EPO; IBM_TDB	AND	ON	2005/03/20 16:56
L4	56	L2 and (UART near2 multiplex\$3)	US-PGPU B; USPAT; USOCR; EPO; IBM_TDB	AND	ON	2005/03/20 18:09
L5	56	L2 and (UART near2 multiplex\$3) and multiplex\$3	US-PGPU B; USPAT; USOCR; EPO; IBM_TDB	AND	ON	2005/03/20 17:21

L6	15	L2 and (UART adj multiplex\$3)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/20 17:21
L7	23	L2 and (UART adj channel)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/20 18:12
L8	16	L7 and multiplex\$3	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/20 18:05
L12	28	L4 and (provide or improve or better)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/20 18:10
S1	11925	709/203,217-219,227. ccls.	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:26
S2	0	headless and rout\$3 and (communicat\$3 or transmi\$5) and Eathernet	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:29

S3	4	route 3 and (communication 3 or transmission 5) and Ethernet	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:29
S4	4	route 3 and Ethernet	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:30
S5	7488	UART	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:31
S6	5128	UART @ad<"20020102"	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/20 16:41
S7	2483	S6 and remote	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:33
S8	1237	S7 and multiplex 3	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:33

S9	330	S7 and (multiplex\$3 near4 control)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:35
S10	4993	S6 not "International Business Machines".as.	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:37
S11	2428	S10 and remote	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:37
S12	326	S11 and (multiplex\$3 near4 control)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:38
S13	1201	S11 and multiplex\$3	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:39
S14	741	S13 and (partition or sect\$3)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:40

S15	146	S14 and Ethernet	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:50
S16	1	S15 and (UART adj channel)	US-PGPU B; USPAT; USOCR; EPO; IBM_TD B	AND	ON	2005/03/18 17:41